IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: To Be Assigned Examiner: To Be Assigned

In Re PATENT APPLICATION Of:

Applicants : Takashi OHSUMI

Serial No. : To Be Assigned

Filed: November 18, 2003

For : SEMICONDUCTOR DEVICE

Attorney Ref. : OKI 389

November 18, 2003

INFORMATION DISCLOSURE

STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

This is an information disclosure statement submitted in compliance with the timing requirements of 37 C.F.R. §1.97(b)(1).

Attached are copies of two Japanese publications. Any relevance of the Japanese publications can be gleaned from the attached English-language Abstracts, and from page 1 of the application, where one of these documents is discussed. The publications are listed on the attached Form PTO-1449.

Since this Information Disclosure Statement is being filed with the application, no certification or fee is required, and the requirements of 37 C.F.R. §§1.97 and 1.98 are deemed to be fully met as to the documents submitted. Consideration of the submitted and listed documents is respectfully requested.

Respectfully submitted,

November 18, 2003

Date

RHB:tl

Robert H. Berdo, Jr. (Reg. No. 38,075)

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FEE ENCLOSED:\$ & Please charge any further fee to our Deposit Account No. 18-0002

FORM PTO-1449 INFORMATION DISCLOSURE STATEMENT				Atty Docket	Applica	Application No. To Be Assigned		
				OKI 389	To Be			
				Applicant Takashi OHSUMI				
								Filing Date
								November 18, 2003
			U.S. P	PATENT DOCUMENTS				
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	AK	JP 2002-093945	03/29/02	Japan	1		Abstract	
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EXAMINER: citation if not	Initial in conf	if reference conside ormance and not con	red, whether o	or not citation is in conformance with MP ude copy of this form with next communication.	EP 609; dr	aw line the	rough	